

AMENDMENTS TO THE SPECIFICATION:

Please amend the paragraph beginning on page 9, line 18, as follows:

A1
The data latch 20₁ latches the corresponding γ -voltage address in synchrony with the γ -clock signal $[[111]] \underline{11}_1$ to deliver the latched address to the decoder 21₁. Similarly, the data latch 20_n latches the corresponding γ -voltage address in synchrony with the γ -clock signal 11_n to deliver the latched address to the decoder 21_n. The γ -voltage address is set at an arbitrary number between zero and L during an initial adjustment, depending on the optical transmittance of the LCD panel.

Please amend the paragraph beginning on page 10, line 1, as follows:

A2
The data latch 23₁ latches the corresponding Vcom-voltage address in synchrony with the COM clock signal $[[121]] \underline{12}_1$, and delivers the latched address to the decoder 24₁. Similarly, the data latch 23_n latches the corresponding Vcom-voltage address in synchrony with the COM clock signal 12_n , and delivers the latched address to the decoder 24_n. The Vcom-voltage address is set at an arbitrary number between zero and L during the initial adjustment, depending on the optical transmittance of the LCD panel.

Please amend the paragraph beginning on page 11, line 19, as follows:

A3 Each decoder and a corresponding multiplexer function as a ~~[[DC]]~~ D/A converter, which receives a digital voltage signal specifying a specific voltage to thereby output an analog voltage signal having a value specified by the digital voltage signal.

Please amend the paragraph beginning on page 16, line 10, as follows:

A4 The multiplexer 221 selects Va(1) among the voltages Va(1)-Va(64) based on the voltage address signal 105, and delivers a voltage Vb(1) corresponding to Va(1). The multiplexer ~~[[222]]~~ 22₂ selects Va(65) among the voltage Va(65)-Va(128), and delivers a voltage Vb(2) corresponding to Va(65). The multiplexer ~~[[223]]~~ 22₃ selects Va(130) among the voltages Va(129)-Va(192) based on the voltage address signal 105, and delivers a voltage Vb(3) corresponding to Va(130). The multiplexer ~~[[224]]~~ 22₄ selects Va(194) among the voltages Va(193)-Va(256) based on the voltage address signal 105, and delivers a voltage Vb(4) corresponding to Va(194).

Please amend the paragraph beginning on page 16, line 21, as follows:

A5 The multiplexer ~~[[251]]~~ 25₁ selects Va(3) among the voltages Va(1)-Va(64) based on the voltage address signal 105, and delivers a voltage Vc(1) corresponding to Va(3). The multiplexer ~~[[252]]~~ 25₂ selects Va(195) among the voltage Va(193)-Va(256), and delivers a Vcom-correction voltage Vc(2) corresponding to Va(195).